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Please amend the claims as follows.

- Sub 1* 1. (Amended Once) A clock generating method for an asynchronous transmission, comprising:
- determining a plurality of actual signal arrival times;
 - averaging the plurality of actual signal arrival times; and
 - correcting a timing of a receiving clock on a basis of an average of the plurality of actual signal arrival times and an expected signal arrival time.
2. (Amended Once) The method according to claim 1, further comprising deriving an expected signal arrival time from the receiving clock.
- Sub 2* 3. (Amended Twice) The method according to claim 1, wherein determining further comprises counting a time period between arrival of a first signal and arrival of a subsequent second signal.
4. (Amended Once) The method according to claim 3, wherein averaging further comprises storing counted time periods and calculating an average of stored time periods.

5. (Amended Twice) The method according to claim 1, wherein correcting further comprises:

determining a frequency difference between a frequency corresponding to an average of the plurality of actual signal arrival times and a frequency of the receiving clock; and

changing the frequency of the receiving clock according to the frequency difference.

SUB B' 6. (Amended Twice) The method according to claim 1, wherein the asynchronous transmission is an ATM transmission and the signal is an ATM cell.

7. (Amended Once) A clock generating apparatus for asynchronous transmission comprising:

means for determining an average of actual signal arrival times and for generating a control signal on a basis of a determined average of the actual signal arrival times and an expected signal arrival time; and

means for correcting a timing of a receiving clock on a basis of the control signal.

8. (Amended Once) The apparatus according to claim 7, wherein the means for correcting comprises a voltage controlled oscillator.

9. (Amended Twice) The apparatus according to claim 7, wherein the means for determining comprises:

means for detecting an actual arrival time of a signal;

means for averaging a plurality of detected actual signal arrival times in order to obtain an average of the actual signal arrival times; and

means for comparing and correction control the average of the actual signal arrival times with the expected signal arrival time and for generating the control signal in accordance with a comparison result, wherein the expected signal arrival time is derived from the receiving clock.

10. (Amended Once) The apparatus according to claim 9, wherein the means for determining comprises means for storing a plurality of detected actual signal arrival times.

11. (Amended Twice) The apparatus according to claim 9, wherein the means for detecting comprises a timer.

12. (Amended Twice) The apparatus according to claim 9, wherein means for comparing and correction control comprises a phase detector, and wherein a polarity of the control signal is changed in accordance with a result of comparison.

13. (Amended Twice) The apparatus according to claim 7, wherein the asynchronous transmission is an ATM transmission and the signal is an ATM cell.